


I.VIVEK ANAND

QUALIFICATION	M.E.	
SPECIALIZATION	VLSI	
DESIGNATION	ASSISTANT PROFESSOR	
DATE OF JOINING THE INSTITUTION	02.06.2014	
TEACHING EXPERIENCE	3 YEARS	

LIST OF INTERNATIONAL JOURNAL PUBLICATIONS

1	Published a research paper title “Design of Low Power Combinational Circuits using Reversible Logic and Realization in Quantum Cellular Automata” in IJIRSET, Volume 3, Special Issue 3, March 2014.
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LIST OF INTERNATIONAL CONFERENCE PUBLICATIONS

1	Presented a paper in the “International Conference ICIECS 2014, in topic Design of Combinational Logic Circuits like Ripple Carry Adder, Carry Delay Multipliers for Low Power Reversible Logic Circuits in Quantum Cellular Automata and Tanner Tools” in Karpagam College of Engineering, Coimbatore.
2	Presented a paper in the “International Conference ICICES 2014, in topic Design of Combinational Logic circuits for Low power Reversible Logic circuits in Quantum Cellular Automata” in S.A. Engineering College of Engineering, Chennai.
3	Presented a paper in the “International Conference ICIET 2014, in topic Design of Low Power Combinational circuits using Reversible Logic and Realization in Quantum Cellular Automata” in K.L.N. College of Engineering, Madurai.

LIST OF NATIONAL CONFERENCE PUBLICATIONS

1	Presented a paper in the “National Conference ESIC 2011, in topic Automated Diagnosis of Glaucoma Using Digital Fundus Images” in PSNA College Of Technology, Dindigul.
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2	Presented a paper in the “National Level Technical Symposium MAESTROZ 2011, in topic Wireless Power Transmission Technologies for Solar Power Satellite” in Roever Engineering College,Elambalur.
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WORKSHOPS/SEMINARS/CONFERENCES/TRAINING PROGRAMMES ATTENDED

S.NO.	NAME OF THE PROGRAMME & VENUE	DATES WITH DURATION	NO. OF PARTICIPANTS	RESPONSIBILITY
1	17 th ISTE TN & P Section Annual Convection & Conferment of Life Time Achievement Awards 2014	07.11.2014 & 08.11.2014	20	PARTICIPANT
2	FDP - Emerging Technologies with Hands on and Live demo	11.11.2014 to 15.11.2014	26	PARTICIPANT
3	Seminar cum Workshop – VLSI Design using CAD Tools	18.12.2014 to 20.12.2014	30	PARTICIPANT
4	FDP – Digital Custom IC Design using Cadence Systems Custom Design Flow	19.2.2015 to 20.2.2015	24	PARTICIPANT
5	Workshop - NPTEL	13.2.2015	20	PARTICIPANT
6	FDP – Principles of Digital Signal Processing	25.5.2015 to 1.6.2015	19	PARTICIPANT
7	Workshop – Advanced Embedded System design on Zynq using Vivado targeting Zed Board	27.8.2015 to 28.8.2015	24	PARTICIPANT
8	Workshop – Internet of Things & Smart Home	13.11.2015, 14.11.2015	20	PARTICIPANT
9	FDP – Digital Communication	30.5.2016 to 6.6.2016	22	PARTICIPANT

10	NPTEL Online Certification – introduction to Research	Jan to Feb 2016	-	PARTICIPANT
11	NPTEL Online Certification – VLSI Design Verification and Test	Aug to Sep 2016	-	PARTICIPANT
12	GIAN Course – Advances in Flexible CMOS Radio Frequency Transceivers	14.11.2016 to 18.11.2016	23	PARTICIPANT
13	Quality Improvement Programme – Instructional Design and Delivery Systems	22.5.2017 to 27.5.2017	24	PARTICIPANT
14	Quality Improvement Programme – Technology Enabled Teaching Learning Process	27.5.2017 to 29.5.2017	20	PARTICIPANT