# Department of Electronics and Communication Engineering

MINUTES of  $19^{\rm th}$  Meeting of the Board of Studies was held on  $30^{\rm th}$  June 2023 at 10.30 A.M

**Venue:** Conference Hall, National Engineering College K.R.Nagar, Kovilpatti – 628503

## National Engineering College, K.R.Nagar, Kovilpatti-628503

(An Autonomous Institution -Affiliated to Anna University, Chennai) www.nec.edu.in

## Department of Electronics and Communication Engineering

19th Board of studies Meeting of the Department of Electronics and Communication Engineering

Venue: Conference Hall, National Engineering College K.R.Nagar, Kovilpatti – 628503

Date & Time: 30.06.2023 & 10.30 A.M

#### **AGENDA**

ECE/BoS 19.1	:	Welcome address and Opening Remarks by Chairman, Board of studies of the Department of Electronics and Communication Engineering		
ECE/BoS 19.2	:	Confirmation of the Minutes and action taken report of the 18 <sup>th</sup> Board of studies meeting of the Department of Electronics and Communication Engineering held on 30 <sup>th</sup> December 2022.		
ECE/BoS 19.3		Business brought forward by the Chairman, Board of studies.  19.3.1. UG degree programme R-2019 Additional elective course.  19.3.2. UG degree programme R-2023 Curriculum structure.  19.3.3. UG degree programme R-2023 Ist semester syllabus.  19.3.4. PG degree programme R-2023 Curriculum structure  19.3.5. PG degree programme R-2023 Ist semester syllabus.		
ECE/BoS 19.4	:	Suggestions given by the BoS Members		

National Engineering College, K.R. Nagar, Kovilpatti – 628 503

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F. No. 1-1/NEC/Branch/ECE

4th July, 2023

Dear Sir/Madam,

Sub: Minutes of the 19th Board of Studies Meeting of Department of Electronics and Communication Engineering - Reg.

The 19th Board of studies Meeting of Electronics and Communication Engineering department of National Engineering College, K.R.Nagar, Kovilpatti – 628503 was held on 30th June 2023 at 10.30 A.M at the conference hall of National Engineering college. Thank you for attending the meeting and giving valuable suggestions. The minutes of the meeting is attached herewith.

Kindly go through the minutes and give your remarks if any. Please acknowledge the receipt of this mail and approve the minutes if there is no comments from your end.

Thanking You,

Yours Sincerely

(Dr.S.Tamil Selvi)

### National Engineering College, K.R.Nagar, Kovilpatti – 628 503

(An Autonomous Institution - Affiliated to Anna University, Chennai)
www.nec.edu.in

# Department of Electronics and Communication Engineering MINUTES OF THE MEETING

The 19<sup>th</sup> Meeting of Board of studies of the Department of Electronics and Communication Engineering was held on 30<sup>th</sup> June, 2023 at 10.30 A.M in the Conference Hall of the National Engineering College, K.R.Nagar, Kovilpatti–628503.

The Board of Studies members are as follows,

1	Dr.S.Tamil Selvi	Chairman		
1.	Professor & Head			
2.	Dr.S.Shanmugavel, Director			
3.	Dr.A.Shenbagavalli, Professor & Dean (Academic)			
4.	Dr. T.S. Arun Samuel, Professor			
5.	Dr.V.Suresh, Asso.Prof	Internal Members		
6.	Dr. K.J.Prasanna Venkatesan, Asso. Prof.			
7.	Mr.T.Devakumar, Asst.Prof (SG)			
8.	Mr. C. Cammillus, Asst. Prof (SG)			
9.	Dr.I.Paulkani, AP			
10.	Dr. R.A. Alaguraja Associate Professor, Department of ECE, Thiagarajar College of Engineering, Madurai – 625 015	Academic Expert nominated by Anna University, Chennai		
11.	Dr.K.Murugan, Professor, Ramanujan Computing Centre, College of Engineering, Anna University, Chennai - 600025.	Academic Experts		
12.	Dr.R.Pandeeswari, Associate Professor, Department of ECE, NIT Trichy - 620015.			
13.	Dr.Klutto Milleth, Chief Technologist, Centre of Excellence in Wireless Technology, IIT Madras Research Park, Chennai – 600113.	Meritorious Alumnus		

1.4	Mr. S. Kaliraj,		
14.	Chief Scientist and Co-Founder,	Meritorious Alumnus	
	Vlinder Labs Pvt Ltd, Bengaluru – 560102.		
15	Mr.S.Madhusudhan,		
15.	Director,		
	Vlinder Labs Pvt Ltd, Bengaluru - 560102.		
	Mr.Irshadh Abdul Rasheed K.A,	Industrial Experts	
16.	Associate Partner Cyber Security,		
	Ernst and Young LLP,		
	Chennai - 600113.		
17.	Mrs.Mehala Balasundaram,	G i I I i	
17.	Senior Technical Writer II,		
	Synopsys, Bangalore - 560016.		
18.	Mr.Rajiv V.K,	Special Invitees	
18.	Senior Director, Test Engineering		
	Tessolve Semiconductor Pvt Ltd, Bengaluru.		
19.	Mr.M.S.Bala Iyyappan, IV Year ECE	Student Members	
20.	Ms.J.Chobia Lakshmi, III Year ECE		

BoS/ECE 19.1	: WELCOME ADDRESS AND OPENING REMARKS B	BY			
	CHAIRMAN, BOARD OF STUDIES OF THE DEPARTMEN	T			
	OF ELECTRONICS AND COMMUNICATION ENGINEERIN	G			
	The Chairman, BoS of the department of Electronics ar	ıd			
	Communication Engineering welcomed and introduced the members of the 19th Board of Studies.	ne			
BoS/ECE 19.2	: TO CONFIRM THE MINUTES OF 18th BOS MEETING HEL	D			
	ON 30th DECEMBER 2022.				
	The minutes of the 18th Board of Studies meeting held on 30	)th			
	December 2022 were communicated to the members. The received comments have been properly analyzed and corrections				
	have been carried out in the syllabus. Then it was presented	in			
	18th Academic Council meeting held on 07th January 2023 and				
	the same was approved.				
	> Action Taken Report				
	(Enclosed in Annexure I)				

BoS/ECE 19.3.1	Consider and approve of the additional elective course & syllabus for U.G. degree programme (B.E) under R 2019
a a a a a a a a a a a a a a a a a a a	Resolved to approve of the additional elective course & syllabus for UG Degree Programme Under (B.E) R-2019.
BoS/ECE 19.3.2	Consider and approve of the Curricula of U.G. degree programme (B.E) under R-2023  Resolved to approve of the Curricula of U.G. degree programme
1	(B.E) under R-2023
BoS/ECE 19.3.3	Consider and approve of the syllabus of I semester U.G. degree programme (B.E) under R-2023
	Resolved to approve of the syllabus of I semester U.G. degree programme (B.E) under R-2023
BoS/ECE 19.3.4	Consider and approve of the Curricula of P.G degree programme (M.E. Embedded System Technologies) under R 2023
	Resolved to approve of the Curricula of P.G degree programme (M.E. Embedded System Technologies) under R 2023.
BoS/ECE19.3.5	Consider and approve of the syllabus of I semester P.G degree programme (M.E. Embedded System Technologies) under R 2023 Resolved to approve of the syllabus of I semester P.G degree programme (M.E. Embedded System Technologies) under R 2023
BoS/ECE 19.4	: SUGGESTIONS GIVEN BY THE BOS MEMBERS
19.4.1	<ul> <li>U.G. degree programme (B.E) under R 2023</li> <li>In the "19EC66E - Theory and applications of Blockchain elective course under Regulation 2019, block chain and its evolution can be introduced in Unit III.</li> <li>Experts suggested to remove the last line ("to prevent these" in the Unit V. Hyperledger may be removed from tutorial and Mearkle tree can be included in the assignment instead of assignment no.4.</li> </ul>

19.4.2	:	$\succ$ In Sem I, the course titled "Introduction to Electronics and
		Communication Engineering" may be changed to
		"Introduction to Engineering".
		➤ In Sem I, the course titled "C programming" may be changed
		to "Problem Solving using programming languages".
19.4.3	:	> The courses titled "Aptitude I" (Sem I) and "Aptitude II" (Sem
		IV) may be considered as non CGPA skill development course
10		or value-added courses.
19.4.4	:	> The courses titled "Electromagnetic Waves" in Sem III and
		courses title "Transmission Lines and Waveguides" in Sem IV
		may be combined to one course.
		> The course titled "Product, Patent and Literature Review" is
		too advanced for the 3 <sup>rd</sup> semester students. Instead of
8		considering it as Integrated Laboratory and Project (ILP)
247		Course it may be changed to theory only course with titled as
		"Design thinking life cycle or Patent, Copyright &
		Trademark". It may be moved to higher semester.
		> The course titled "Probability Random Process and Queuing
		Theory" may be changed to "Random Process and Queuing
		Theory ".
		> Experts suggested to include the Lab courses for the course
		titled "Signals and Systems".
		> Experts suggested to move the course titled "Microprocessor
		and Microcontroller" from Sem V to Sem III and the course
		titled "Computer Networks" can be included in Sem IV.
19.4.5	;	> Experts suggested that the integrated Theory and Laboratory
		(ITL) Courses for the core courses may be revised into Theory
30		only (TO) and Laboratory only (LO) courses.
		> Cloud, Artificial Intelligence and Machine Learning can be
		included as programme core courses in the curriculum.

19.4.6	P.G degree programme (M.E. Embedded System Technologies)
	under R 2023
	> In Sem I, the course titled "ARM CortexM3 Architecture" can
	be replaced by a generic title and it may be focused on the introduction to recent architecture in syllabus.
19.4.7	> Experts suggested to include the FPGA based design using
8	Verilog or any other languages as theory course in the Sem I or Sem II as core course.
	Also suggested to include middleware and application part of embedded systems in curriculum.
	> Artificial Intelligence (AI) & Machine learning (ML) may be introduced as elective courses.
	introduced as elective courses.

The members had a brainstorming discussion and interaction among themselves. Based on the suggestions given by the members, BOS resolved to recommend the following to the Academic Council for further approval.

- I. UG degree programme R-2019 Additional elective course.
- 2. UG degree programme R-2023 Curriculum structure.
- 3. UG degree programme R-2023 I\* semester syllabus.
- 4. PG degree programme R-2023 Curriculum structure.
- 5. PG degree programme R-2023 Ist semester syllabus.
- 6. Any other matter.

Dr.V.Suresh, Associate Professor / ECE thanked all the members for their kind cooperation and the meeting came to an end.

#### Members Present:

	Chairman	Signature
I.	Dr. S. Tamil Selvi,	
	Professor & Head,	P.J. pho
	Department of ECE, National Engineering College, Kovilpatti.	301
	Internal Members	
2.	Dr.S. Shanmugavel, Director	Bryml 30/6/2
3.	Dr.A. Shenbagavalli, Professor & Dean (Academic)	Anni
4.	Dr. T.S. Arun Samuel, Professor	Mildre Aus
5.	Dr. V. Suresh, Asso.Prof	1 30/0b/
6.	Dr. K.J. Prasanna Venkatesan, Asso. Prof	F 30 Job 2023
7.	Mr.T. Devakumar, Asst.Prof (SG)	(de)
8.	Mr. S. Cammillus, Asst. Prof (SG)	1921
9.	Dr.I. Paulkani, AP	Suit 123
	Academic Expert nominated by Anna University, Che	ennai 7 9
10.	Dr. R.A. Alaguraja	Malo 30/06/
	Associate professor, Department of ECE,	30/06/
	Thiagarajar College of Engineering, Madurai - 625 015	
	Academic Experts	
II.	Dr.K.Murugan,	
	Professor, Ramanujan Computing Centre,	1.1
	College of Engineering,	1 pulses 11
	Anna University, Chennai - 600025.	K Nordan Bolool

12.	Dr.R.Pandeeswari,	Va Roman
	Associate Professor, Department of ECE,	1 / Car
	NIT, Trichy - 620015.	
	Meritorious Alumnus	
13.	Dr.Klutto Milleth,	
	Chief Technologist, Centre of Excellence in Wireless Technology,	
	IIT Madras Research Park,	10
	Chennai – 600II3.	
I4.	Mr. S. Kaliraj, Chief Scientist and Co-Founder,	1 A live
	Vlinder Labs Pvt Ltd, Bengaluru - 560102.	5 mbalue
	Industrial Experts	
15.	Mr.S.Madhusudhan,	1 0 M. D.TG
	Director, Vlinder Labs Pvt Ltd, Bengaluru - 560102.	De man 2
16.	Mr.Irshadh Abdul Rasheed K.A,	N.
	Associate Partner Cyber Security,	KA JA
	Ernst and Young LLP, Chennai - 600113.	1
	Special Invitees	
17.	Mrs.Mehala Balasundaram,	
	Sr.Technical Writer II	Rythan
	Synopsys, Bangalore - 560016.	1
18.	Mr. V.K.Rajiv, Senior Director, Test Engineering	W W
	Tessolve Semiconductor Pvt Ltd, Bengaluru.	0
	Student Members	
19.	Mr.M.S.Bala Iyyappan, IV Year ECE	Balowyyapen
20.	Ms.J.Chobia Lakshmi, III Year ECE	1,010

S.J.phy. CHAIRMAN

BOARD OF STUDIES

Annexure – I

Minutes of 18<sup>th</sup> BoS Meeting - Action Taken Report

S. No	Suggestion given by expert members	Action taken
1.	➤ In the "19EC71E — Web Application Development" elective course under Regulation 2019, equal weightage (marks) may be given to the practical content, and one of the evaluators for the practical content may be an expert from the industry.	Implemented
2.	<ul> <li>In Vertical 1 – VLSI (under Regulation 2019) proposed in the 18<sup>th</sup> BoS meeting, the course titled "Hardware Modelling using Verilog HDL", may be changed to an integrated course (Theory + Lab).</li> <li>The program Elective course "19EC37E – Fundamentals of Semiconductor Chip Testing" may be included in VLSI Vertical.</li> <li>An introduction to the DFT tools may be included in the course titled "Design for Testability (DFT)."</li> </ul>	Implemented
3.	<ul> <li>In Vertical 2 – Signal Processing proposed in the 18th BoS meeting, the experts suggested to revise the vertical name as "Signal Processing and its Applications."</li> <li>Experts suggested that three courses related to the applications of signal processing in Communication may be included in this vertical.</li> </ul>	Implemented
4.	<ul> <li>In Vertical 3 - Cyber Security proposed in the 18th BoS meeting, Experts suggested to do the following modifications in the Cyber Crime investigations and Digital forensics Course:</li> <li>1. Few topics are redundant in Unit 1 and Unit 2 and it may be removed from one unit.</li> <li>2. The textbooks given for each course may be updated to the latest version.</li> </ul>	Implemented

- > In the 2019 curriculum, 19EC81C-Project work II (6 credits) was approved in the previous BoS meeting.
- ➤ In the 18<sup>th</sup> BoS meeting, the proposal was placed before the BoS members to replace Project Work II as Project Work II/Industry Practice. The BoS members approved this proposal with the following suggestions.
  - For industry practice, a report by the student approved by an industry mentor or certificate from the industry stating that the student has done industry practice in the respective industry part of the placement.
  - The periodical and end-semester assessment for industry practice may be done with one examiner from academia and another from the industry. This assessment may be in online mode.
  - The Project work II assessment, the practice followed in regulation 2019 be continued.

Implemented

S.J. The