


N.ARUMUGAM

QUALIFICATION	M.E., (Ph.D)	
SPECIALIZATION	VLSI AND SENSOR NETWORKS	
DESIGNATION	ASSOCIATE PROFESSOR	
DATE OF JOINING THE INSTITUTION	12.10.1988	
TEACHING EXPERIENCE	32 YEARS	

FUNDED PROJECT / CONSULTANCY WORK UNDERTAKEN

S.NO.	NAME OF THE PROJECT AND FUNDING ORGANIZATION	DURATION WITH YEARS	AMOUNT SANCTIONED	RESPONSIBILITY
1	FULLY AUTOMATED COCONUT HARVESTING MACHINE ORG: Department of Science and Technology(DST,NewDelhi) Section: Instrumentation Development Program(IDP) Ref: IDP/IND/18/2013/Rs 2441360/- /SCHEME:TNSCST-DST/ Industry Partner: M/S ELECTRNO SOLUTIONS,BANGALORE	3YEARS (AUGUST 2015 TO AUGUST2018)	Rs 2441360/-	CO-PRINCIPAL INVESTIGATOR

LIST OF INTERNATIONAL JOURNAL PUBLICATIONS

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1.	T.S.Arun Samuel, N. Arumugam and S.Theodore Chandra 2017 “Analytical Approach and Simulation of GaN Single Gate TFET and Gate All around TFET”, ECTI transactions on electrical eng., electronics, and communications , Vol.15, No.02, pp. 1-7. (ISSN: 1685-9545)
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2.	T.S. Arun Samuel, S. Darwin and N. Arumugam 2017, “Design of adiabatic logic based comparator for low power and high speed applications”, ICTACT Journal On Microelectronics , Vol.3, No.1, 365-369. (ISSN: 0925-1030)
3.	T.S. Arun Samuel, N. Arumugam and A. Shenbagavalli, 2016, “Drain Current Characteristics Of Silicon Nanowire Field Effect transistor”, ICTACT Journal On Microelectronics , Vol.2, No.3, pp. 284-287 (ISSN 2395-1680).
4.	N.Arumugam Histogram Modified Local Contrast Enhancement For Mamogram Images, Applied Soft Computing , Vol 11, 2011, pp5809-5816, ISSN:1568-4946 .
5.	N.Arumugam A New Technique to Efficient water consumption system using ARM7, International Journal of Research in Electronics , vol.1, pp16-22, 2015 (ISSN: 2349-252X)

INTERNATIONAL CONFERENCE PUBLICATIONS

S.No	Title of the Paper	Name of the Conference	Organizer	Month & Year
1	An approach to content dependant Intrusion detection.	ROVPIA'99	ROVPIA'99, EPOH, Malaysia	APRIL 1999
2	A survey on Energy Efficient sensor node design for IoT applications	ICAEASM-2017	Centre for development of advanced computing, Mumbai	August 2017
3	Comparative study on sensor node for secured communication in wireless sensor network	ICAEASM-2017	Centre for development of advanced computing, Mumbai	August 2017

**WORKSHOPS/SEMINARS/CONFERENCES/TRAINING PROGRAMMES
ORGANIZED**

S.NO.	NAME OF THE PROGRAMME & VENUE	DATES WITH DURATION	NO. OF PARTICIPANTS	RESPONSIBILITY
1	Anna University FDTP on EC6601-VLSI Design National Engineering College, Kovilpatti	01.12.2015 TO 08.12.2015	32	Coordinator
2	Faculty Development Programme on 'Advanced VLSI Design' National Engineering College, Kovilpatti	15.07.2013 TO 20.07.2013.	28	Coordinator

EXPERT LECTURES DELIVERED:

S.No	Title	Venue	Date	Participants
1.	"Correlative Coding" In FDP On DIGITAL COMMUNICATION	NATIONAL ENGG. COLLEGE	09-11-2005	ECE Faculties of Engg.Colleges
2.	Vlsi & Embedded Systems	Kamarajar College Of Engg & Tech, Virudhunagar	FROM 03-11-2008 TO 08-11-2008	ECE STUDENTS
3	Microprocessors	Cape Institute Of Technology, Leveengipuram	31-03-2009	EEE STUDENTS
4	Embedded Systems	Kamarajar College Of Engg & Tech, Virudhunagar	17th & 18th of DEC 2009	ECE STUDENTS
5	Arm Processor And Interfacing	P.S.R. Engg.College	21-01-2010	ECE STUDENTS
6	Computer Organisation	Fdp Programme Anna University, Trichy	27-03-2010	FACULTIES
7	Arm Based Embedded System Development	N.P.A. Centenary Polytechnic College, Kotagiri	3 DAYS 09-03-11 TO 11-03-11	STAFF & STUDENTS
8	VLSI DESIGN	Cape Institute Of Technology, Leveengipuram	12-03-2011	STUDENTS & STAFF

9	VLSI DESIGN	Kalasalingam Institute Of Technology, Krishnan Kovil	05-05-2011	STUDENTS
10	VLSI DESIGN	Scad College Of Engineering And Technology, Cheranmahadevi.	01-04-2011	STUDENTS
11	VLSI DESIGN	Kamarajar College Of Engg, Virodhunagar	06-04-2011	STUDENTS
12	VLSI DESIGN	Jayamadha Engg.College, Aralvaimozhi	09-04-2011	STUDENTS
13	VLSI Design Techniques	Scad College Of Engg &Tech, Cheranmahadevi	06-05-2011	STAFF & STUDENTS
14	Arm Processor Based Embedded Systems	Einstein Engg. College, Tirunelveli	01-10-2011	FACULTIES
15	FPGA Based Controller For Power Electronics	National Engg.College Eee Dept	15-10-2011	FACULTIES
16	FPGA Based Controller For Power Electronics	V.S.V.N. Polytechnic, Virudhunagar		FACULTIES
17	Vlsi Design	Sri Vidhya Engineering College, Viruthunagar	13-03-2012	STAFF & STUDENTS

Membership in Professional bodies :

Name of Society	Grade of Membership
I.S.T.E.	LIFE