

**PERSONAL DETAILS AND AFFILIATION**

<b>Name of the Faculty Member</b>	KARTHIKA.S	
<b>Designation</b>	ASSISTANT PROFESSOR	
<b>Department</b>	Electronics and Communication Engineering	
<b>Date of joining the institution</b>	13.06.2013	
<b>Date of Birth</b>	-	
<b>Address for Communication</b>	Department of ECE, National Engineering College, K.R. Nagar, Kovilpatti-628 503	
<b>Email ID/webpage ID</b>	skece@nec.edu.in	

**EDUCATIONAL QUALIFICATION**

<b>DEGREE</b>	<b>BRANCH</b>	<b>UNIVERSITY</b>	<b>YEAR</b>
B.E.	VICKRAM COLLEGE OF ENGINEERING	ANNA UNIVERSITY, TIRUCHIRAPALLI	2011
M.E.	BANNARI AMMAN INSTITUTE OF TECHNOLOGY	ANNA UNIVERSITY, CHENNAI	2013
Ph.D.	Pursuing	ANNA UNIVERSITY, CHENNAI	-

**EXPERIENCE**

<b>S.NO.</b>	<b>NAME OF THE INSTITUTION / ORGANIZATION</b>	<b>POSITION HELD</b>	<b>FROM</b>	<b>TO</b>	<b>EXPERIENCE</b>	
					<b>Y</b>	<b>M</b>
1.	NATIONAL ENGINEERING COLLEGE, KOVILPATTI	ASSISTANT PROFESSOR	13.06.2013	Till date	8	5
<b>TOTAL EXPERIENCE</b>					8	5

## PUBLICATIONS

### JOURNALS

1.	<b>“Design and Analysis of Low Power Compressors”</b> was published in International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE) VOLUME 1, ISSUE 6, December 2012
2.	<b>“Cardless ATM”</b> was published in the International Research Journal of Engineering and Technology (IRJET). Volume 7, Issue3, March 2020
3.	<b>“Design and Implementation of BRAM memory for reconfigurable Applications”</b> was published in Turkish Online Journal of Qualitative Inquiry (TOJQI), Volume 12, Issue 6, June 2021

### CONFERENCES

1.	<b>“Design and Analysis of Low Power Compressors”</b> was accepted in the proceedings of Second International Conference on Innovative Research in Engineering and Technology (ICIRET 2013) at Park College of Engineering and Technology, Coimbatore.
2.	<b>“Performance and Power Analysis of Different Low Power Compressors”</b> was accepted in the proceedings of International Conference on Innovations in Intelligent Instrumentation, Optimization and Signal Processing at Karunya University, Coimbatore
3.	<b>“Design and Implementation of BRAM memory for reconfigurable Applications”</b> was presented in the International E-Conference on recent Trends Advances in Computation, Communication, Internet of Things and Artificial Intelligence organized by M.Kumarasamy College of Engineering from 31.03.2021 to 01.04.2021
4.	<b>“A Survey of Embedded memory Testing”</b> was presented in 5th IEEE International Test Conference, India 2021 from 18.07.2021 to 20.07.2021

### WORKSHOPS/SEMINARS/CONFERENCES/TRAINING PROGRAMMES ORGANIZED (If any)

S.NO.	NAME OF THE PROGRAMME & VENUE	DATES WITH DURATION	NO. OF PARTICIPANTS	RESPONSIBILITY
1.	ICMR Sponsored National Level Seminar – Research Issues in Thermography	04.08.2013, 05.08.2013	25	Participant
2.	National Seminar – Research Issues &Challengers in Cloud Computing & Big Data Analysis	22.05.2014	35	Participant
3.	FDP – Emerging Technologies with Hands on Live Demo	11.11.2014 to 15.11.2014	35	Participant
4.	17 <sup>th</sup> ISTE TN & P Section Annual Convention & Conferment of Life Time Achievement Awards – 2014 – Empowering Indian Engineering Education for Global Expectations.	07.11.2014 & 08.11.2014	40	Participant
5.	NPTEL Workshop	13.07.2015	20	Participant

6.	FDP – Principles of Digital Signal Processing	25.05.2015 to 01.06.2015	20	Participant
7.	FDP – VLSI Design	01.12.2015 to 08.12.2015	20	Participant
8.	NPTEL Online course – Introduction to Research	Jan to Feb 2016		
9.	STTP- Medical Imaging and Informatics	20.04.2016 to 26.04.2016	35	Participant
10.	NPTEL Online course – VLSI Design Verification and Test	July to Oct 2016		
11.	STTP – CMOS, Mixed Signal and Radio Frequency VLSI Design	30.01.2017 to 04.02.2017	40	Participant
12.	FDP – Effective Teaching and Learning of Digital Electronics	21.03.2017 to 25.03.2017	50	Participant
13.	NITTTR Training Programme – Instructional Design and Delivery systems.	22.05.2017 to 27.05.2017	50	Participant
14.	NITTTR Training Programme – Technology Enabled Teaching Learning Process	27.05.2017 to 29.05.2017	50	Participant
15.	FDP – Data Science Research and Big Data Analytics	11.12.2017 to 23.12.2017	25	Participant
16.	GIAN Course – Advanced CMOS Clock Generation Circuits	25.12.2017 to 29.12.2017	50	Participant
17.	NPTEL Online course – Integrated Circuits, MOSFETs, Op-Amps and their Applications	Jan to Apr 2018		
18.	NPTEL Online course – Analog Circuits	Feb to Mar 2018		
19.	FDP – Xilinx SoC: FPGA Based Design	30.07.2018 to 03.08.2018	40	Participant
20.	NPTEL Online Course – Digital Circuits	Jul to Oct 2018		
21.	FDP – Nanoscale Devices and Circuits	17.06.2019 to 22.06.2019	40	Participant

#### INDUSTRY TRAINING PROGRAMMES ATTENDED (If any)

S.NO.	NAME OF THE PROGRAMME	NAME OF THE INDUSTRY	DATES WITH DURATION
1	Industrial Training – Analog IC Design and Layout design using Cadence	Entuple Technologies	13.06.2018 to 17.06.2018
2	Industrial Training	Coramandal Electronics	24.02.2020 to 28.02.2020
3.	Industrial Training - Custom IC Design: CMOS Standard Cell Circuit Design, Simulation And Layout	Entuple Technologies	08.07.2020 to 04.09.2020
4.	Industrial Training – Design and verification Using Verilog	Entuple Technologies	09.06.2021 to 14.07.2021

