

PERSONAL DETAILS AND AFFILIATION

Name of the Faculty Member	Kalieswari C	
Designation	Assistant Professor (Senior Grade)	
Department	Electronics and Communication Engineering	
Date of joining the institution	15.05.2019	
Address for Communication	Department of ECE, National Engineering College, K.R. Nagar, Kovilpatti-628 503	
Email ID	kalieswari_ece@nec.edu.in	
Scopus Id:	56497116000	
Orchid ID:	0009-0005-9235-1282	

EDUCATIONAL QUALIFICATION

S.NO.	QUALIFICATION	INSTITUTION STUDIED	UNIVERSITY / BOARD	YEAR OF PASSING
1	B.E. (ECE)	RVS College of Engineering and Technology, Dindigul	Anna University	2012
2	M.E./M.Tech.	RMK Engineering College, Chennai	Anna University	2014

EXPERIENCE

S.NO.	NAME OF THE INSTITUTION / ORGANIZATION	POSITION HELD	FROM	TO	EXPERIENCE	
					Y	M
1	National Engineering College, Kovilpatti	Assistant Professor (Senior Grade)	01.07.2026	Till date		
2	National Engineering College, Kovilpatti	Assistant Professor	15.05.2019	30.06.2026	7	1
3	Dr.Sivanthi Aditanar College of Engineering, Tiruchendur	Assistant Professor	02.01.2015	07.11.2017	2	10

LIST OF FUNDED PROJECTS

S.NO.	NAME OF THE PROJECT	BUDGET OUTLAY	FUNDING AGENCY	STATUS
1	NavIC Signal Based Guidance System	Rs. 2,50,000	NewGen IEDC	Completed
2	In Campus Navigation Assistive System for Blind School Students	Rs 20,47,300	IIT-TNiF Scheme	Ongoing

LIST OF INTERNATIONAL JOURNAL PUBLICATIONS

1	Devakumar T, Kalieswari C , Ferdin Philo Raja TA (2026), NavIC-based Assistive Navigation System for visually impaired individuals, 2026 8th International Conference on Inventive Material Science and Applications (ICIMA), IEEE, May 2026, DOI: 10.1109/ICIMA68728.2026.11564319.
2	Subhiksha S, Jenisha Merlin R, Jeba Christiya A, Kalieswari C (2025), Energy Efficient Approximate MAC architecture using segmentation-based adder and probability driven 4:2 compressor-based multiplier, IEEE 9th International Conference on Information and Communication Technology (CICT), IEEE, DOI:10.1109/CICT67193.2025.11398902
3	K.G.Shanthi, Dr.N.Nagarajan, C.Kalieswari (2014), FPGA realization of high-speed FIR filter based on Distributed Arithmetic. International Journal of Engineering and Technology, Volume 6, No3, Pages 1407 -, 1414, Jun-July 2014. [IF: 0.178]
4	Kalieswari C , A. Joe (2016). Multiple Fusion Based Single Image Dehazing. International Journal of Recent Research and Applied Studies, ISSN: 2349 – 4891, Volume 3, Issue 3(13), Pages 51-55, 2016. [IF: 2.665]
5	Ajay Muthuvelan, Ashish Raja Pillai Muthaiah, Twinkle Gopinath Gurusamy, Josephine Pon Gloria Jeyaraj, C. Kalieswari (2020), ‘Multiband Printed Koch Antenna Unified with Hilbert Split-Ring Structure for Better Front-to-Back Ratio. International Journal of Recent Technology and Engineering (IJRTE), ISSN: 2277-3878 (Online), Volume-9 Issue-1, May 2020. Page No.: 410-415.
6	R. Gayathri, C. Kalieswari (2020), ‘Multi-User Chat Application’. International Journal of Engineering and Advanced Technology (IJEAT), ISSN: 2249-8958 (Online), Volume-9 Issue-5, June 2020, Page No.372-374.

LIST OF INTERNATIONAL CONFERENCE PUBLICATIONS			
S.NO.	TITLE	PERIOD	NAME OF THE PROGRAMME & VENUE
1	Energy Efficient Approximate MAC architecture using segmentation-based adder and probability driven 4:2 compressor-based multiplier - IEEE Conference Publication IEEE Xplore	19.12.2024 to 21.12.2025	CICT 2025, IITDM Kancheepuram
2	High level synthesis of Neural network for rainfall detection	18.12.2025 to 19.12.2025	SCOPE 2025, Thiagarajar College of Engineering, Madurai
3	Hybrid Renewable Energy Generation with Power Electronics	28.03.2025 & 29.03.2025	IC3C-2025, CSI College of Engineering, Ooty.
4	Rectangular Microstrip Patch Antenna	21.03.2025 to 22.03.2025	ICCI-2025 K.Ramakrishnan College of Engineering, Trichy.
5	Smart Fish Farming System	09.03.2023 to 11.03.2023	ICAISC 2023 & Bannari Amman Institute of Technology, Erode.
6	A Wavenet for signal processing	28.05.2022 to 29.05.2022	ETICT 2022 & Sethu Institute of Technology, Kariapatti.
7	Systolic based high speed and area efficient FIR architecture using distributed arithmetic	28.03.2017	NCEA2017 & P.S.R.Rengasamy College of Engineering for Women, Sivakasi.
8	Distributed arithmetic based high throughput and hardware efficient FIR architecture	24.03.2017	NCRTE2C2T-2K17 & Universal College of Engineering and Technology.
9	Low Delay and Hardware Efficient Implementation of Distributed Arithmetic Based FIR Filter	12.04.2014	ICRTIT 2014 & Madras Institute of Technology, Anna University.
10	High throughput and Area efficient FPGA implementation of FIR filter using distributed arithmetic	20.02.2014	ICACT2014 & JKKN College of Engineering and Technology.

LIST OF NPTEL COURSES COMPLETED:			
S.NO.	TITLE	PERIOD	REMARKS
1.	VLSI physical Design	Jan-Apr 2026	Elite + Silver + 2% Topper
2.	Microprocessors and Interfacing	Jan-April 2025	Elite + Gold + 1% Topper
3.	CMOS Digital VLSI Design	Jan-Mar 2025	Elite + Silver + 5% Topper
4.	Design Thinking – A Primer	Jul-Aug 2023	Elite + Silver
5.	Hardware Modeling using Verilog	Jul -Sept 2023	Elite + Silver
6.	Microprocessors and Microcontrollers	Jan-Apr 2023	Elite + Silver + 5% Topper

7.	CMOS Digital VLSI Design	Jan-Mar 2023	Elite + Silver
8.	Digital Circuits	Jul-Oct 2022	Elite
9.	System Design through Verilog	Jul-Sep 2021	Elite + Gold + Topper
10.	Digital VLSI Testing	Sep-Dec 2020	Elite

WORKSHOPS/SEMINARS /TRAINING PROGRAMMES ATTENDED		
S.NO.	NAME OF THE PROGRAMME & VENUE	DATES WITH DURATION
1.	FDP on “AI Driven Innovations in Communication & VLSI- Trends and challenges” at Kamala institute of technology & science, Telangana	15.12.2025 to 20.12.2025
2.	FDP on “Semiconductor Devices and Fabrication and Layout Design” by E & ICT Academy at National Engineering College, Kovilpatti	08.12.2025 to 12.12.2025
3.	5th One Week Workshop on VLSI Device, Circuit and System Design Tools (Online), organized by the School of Electronics Engineering (SENSE), VIT-AP University, Amaravati	26.06.2025 to 02.07.2025
4.	FDP on “Generative and ethical AI: Techniques and Applications with hands-on” at National Institute of Technology, Trichirappalli	20.01.2025 to 25.01.2025
5.	NPTEL FDP Course on “CMOS Digital VLSI Design”	Jan-Mar 2025 (8 Weeks)
6.	FDP on Innovative Teaching Strategies with AI and Digital Tools	30.10.2024
7.	NPTEL FDP Course on “Hardware Modeling using Verilog”	Jan-Apr 2023 (8 Weeks)
8.	NPTEL FDP Course on “Design Thinking – A Primer”	July-Aug 2023 (4 Weeks)
9.	NPTEL FDP Course on “CMOS Digital VLSI Design”	Jan – March 2023 (8 Weeks)
10.	Faculty Development Program on “ Embedded Systems” by NIT Warangal	16.08.2022 to 25.08.2022
11.	Faculty Development Program on “EDA Tools for Industry 4.0 and its applications” by NIT Warangal	18.07.2022 to 27.07.2022
12.	Industrial training program on Design and Verification Using Verilog	06.05.2022 to 17.06.2022
13.	Webinar on “VLSI SoC Design using open-source EDA Tools” organized by PSG Institute of Technology and Applied research, Coimbatore	04.07.2020
14.	Faculty Development Program on ‘VIRTUAL LABS’ at NITK Surathkal	10.08.2020 to 14.08.2020

15.	One Week Online Short-Term Course on Advances in Communication systems for Integrated Technology	06.07.2020 to 10.07.2020
16.	Attended 4th IEEE International Test Conference India 2020	12.07.2020 to 14.07.2020
17.	NPTEL FDP Course on “Digital VLSI Testing”	Sep-Dec 2020 (12 Weeks)
18.	Lecture on “ Trends and Challenges in Nano electronic for the next decade “ delivery by prof. Elena Gnani, University of Bologna, Italy. (organized by IEEE-EDS Delhi Chapter)	20.05.2020
19.	Curious Program on “Med Tech” at IIT, madras	26.09.2019 & 27.09.2019
20.	Six Days Faculty Development program on “Digital Communication” at National Engineering College, Kovilpatti	27.05.2019 to 01.06.2019
21.	National workshop on “Controllers and Communication System Design using LABVIEW” at Dr.Sivanthi Aditanar College of Engineering	01.04.2017
22.	New Faculty Induction workshop at Dr.Sivanthi Aditanar College of Engineering	08.07.2016 & 09.07.2016
23.	Faculty Development Programme on“Analog,MCU,Webench and connectivity” by Texas Instruments,Bangalore at Mepco Schlenk Engineering College	15.06.2016 & 18.06.2016
24.	National workshop“Signal and Image Processing using MATLAB”, Dr.Sivanthi Aditanar College of Engineering	26.09.2016
25.	One day national seminar on “ Trends in Antenna Design & Application”	11.04.2016
26.	“Industry oriented workshop” conducted by Mistral Solutions	10.12.2015
27.	Faculty development program on “Embedded Systems using ARM7 Micro Controller”, Dr.Sivanthi Aditanar College of Engineering	01.12.2015 & 02.12.2015
28.	Two days national workshop on “ARM7 with Hands on Training”, Dr.Sivanthi Aditanar College of Engineering	29.09.2015 & 30.09.2015
29.	One-day workshop on “Matlab” held at Anna University, Chennai	12.03.2013
30.	Two days workshop on “VLSI Design using Cadence EDA Tools” organized by Cadence Design Systems India Pvt. Ltd	11.12.2012 & 12.12.2012
31.	Two days workshop on “Android” at VIT University, Vellore	30.07.2011 & 31.07.2011

SPECIFIC ACHIEVMENTS (IF ANY)	
S.NO.	ACHIEVMENTS
1	Secured 13 th university rank in post-graduation
2	Secured 340 score in GATE 2013.
3	NPTEL Course – System Design through Verilog – Course Topper (97%)
4	Recognized as NPTEL Discipline Star (Jan- April 2023)

MEMBERSHIP DETAILS	
S.NO.	MEMBERSHIP DETAILS
1	Life time member of ISTE (Indian Society for technical education)

SIGNATURE